

:Separator =

:Name = ModBus TCP

:Driver Type = MODBUS TCP (TCP)

:Parameters

:Host = 192.168.0.66

:Port = 502

:UpdateTime = 100

:Channels =

Name	DeviceID	Type	Address	WordSize	Content Type
Button1	1	Coil	0		
Button2	1	Coil	1		
Button3	1	Coil	2		
Button17	1	Coil	16		
Button80	1	Coil	79		
wField1	1	Holding register	100	Word(16bit)	low Endian
wField2	1	Holding register	101	Word(16bit)	low Endian
wField50	1	Holding register	149	Word(16bit)	low Endian
dwField1	1	Holding register	300	DWord(32bit)	low Endian
dwField2	1	Holding register	302	DWord(32bit)	low Endian
dwField25	1	Holding register	348	DWord(32bit)	low Endian
fField1	1	Holding register	500	Float(32bit)	low Endian
fField2	1	Holding register	502	Float(32bit)	low Endian
fField1	1	Holding register	548	Float(32bit)	low Endian

Q_Modbus	bit		
0	1	I_bIridium[1]	BOOL
0	2	I_bIridium[2]	BOOL
0	3	I_bIridium[3]	BOOL
1	0	I_bIridium[17]	BOOL
4	15	I_bIridium[80]	BOOL
100		I_wIridium[1]	WORD
101		I_wIridium[2]	WORD
149		I_wIridium[50]	WORD
300 & 301		I_dwIridium[1]	DWORD
302 & 303		I_dwIridium[2]	DWORD
348 & 349		I_dwIridium[50]	DWORD
500 & 501		I_fIridium[1]	REAL
502 & 503		I_fIridium[2]	REAL
548 & 549		I_fIridium[50]	REAL

:Feedback =

Name	DeviceID	Type	Address	Word Size	Content Type
Lamp1	1	Coil	160		
Lamp2	1	Coil	161		

10	0	Q_bIridium[1]	BOOL
10	1	Q_bIridium[2]	BOOL

Lamp3	1	Coil	162		
Lamp17	1	Coil	176		
Lamp80	1	Coil	239		
wDisplay1	1	Holding register	200	Word(16 bit)	Low Endian
wDisplay2	1	Holding register	201	Word(16 bit)	Low Endian
wDisplay50	1	Holding register	249	Word(16 bit)	Low Endian
dwDisplay1	1	Holding register	400	DWord(32 bit)	Low Endian
dwDisplay2	1	Holding register	402	DWord(32 bit)	Low Endian
dwDisplay25	1	Holding register	448	DWord(32 bit)	Low Endian
fDisplay1	1	Holding register	600	Float(32bit)	Low Endian
fDisplay2	1	Holding register	602	Float(32bit)	Low Endian
fDisplay25	1	Holding register	648	Float(32bit)	Low Endian

10	2	Q_bIridium[3]	BOOL
11	0	Q_bIridium[17]	BOOL
14	15	Q_bIridium[80]	BOOL
200		Q_wIridium[1]	WORD
201		Q_wIridium[2]	WORD
249		Q_wIridium[50]	WORD
400 & 401		Q_dwIridium[1]	DWORD
402 & 403		Q_dwIridium[2]	DWORD
448 & 449		Q_dwIridium[50]	DWORD
600 & 601		Q_fIridium[1]	REAL
602 & 603		Q_fIridium[2]	REAL
648 & 649		Q_fIridium[50]	REAL

Memory Mapping

		Address		bit		Aantal
		from	to	from	to	
Send bit Command in iridium	Bit	0	4	0	79	80 bits
Feedback bit in iridium	Bit	10	14	80	159	80 bits
Send word command in iridium	word	100	149			50 words
Feedback word in iridium	word	200	249			50 words
Send double word in iridium	dword	300	349			25 double words
Feedback double word in iridium	dword	400	449			25 double words
Send real command in iridium	float	500	549			25 float
Feedback real in iridium	float	600	649			25 float